



RoHS Compliant

Features

- High stability and high reliability
- 2.7 to 5.5V drive available
- Clipped sine wave or CMOS level output
- Low phase noise
- Disable Function (KT7050A)

Applications

- Femtocell, Stratum3

How to Order

For Femtocell (Standard Spec.)

Freq. Temp. Chrst. : $\pm 0.1 \times 10^{-6} / -10^{\circ}\text{C}$ to 70°C

KT7050 A 26000 A G T 33 T xx
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

For Stratum3 (Standard Spec.)

Freq. Temp. Chrst. : $\pm 0.28 \times 10^{-6} / -40^{\circ}\text{C}$ to 85°C

KT7050 A 26000 K A W 33 T xx
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

| | |
|-----------------------------|-------------------------|
| ① Series | ⑥ Upper Operating Temp. |
| ② Land Type | T +70°C |
| A 10Pads | W +85°C |
| B 4Pads | ⑦ Supply Voltage |
| ③ Output Frequency | 33 3.3V |
| ④ Freq. Temp. Chrst. | ⑧ Voltage Control Range |
| A $+0.1 \times 10^{-6}$ | T TCXO |
| K $\pm 0.28 \times 10^{-6}$ | Other* VCTCXO |
| ⑤ Lower Operating Temp. | * Customer Spec. |
| A -40°C | ⑨ Option Code |
| G -10°C | |
| J 0°C | |

Packaging (Tape & Reel 1000 pcs./ reel)

Specifications

| Item | Symbol | Conditions | Min. | Max. | Units |
|--|-------------------|---|---------------------|---------------------|------------------|
| Output Frequency Range | fo | Standard Frequency: 10, 12.8, 16.368, 19.2, 19.44, 20, 20.48, 26 | — | — | MHz |
| Frequency Tolerance | f _{tol} | vs Temperature (-10 to +70°C) $[\pm(f_{\text{max}}-f_{\text{min}})/ 2f_0]$ | -0.1 | +0.1 | $\times 10^{-6}$ |
| | | vs Temperature (-40 to +85°C) $[\pm(f_{\text{max}}-f_{\text{min}})/ 2f_0]$ | -0.28 | +0.28 | |
| | | vs Voltage | -0.1 | +0.1 | |
| Supply Voltage | V _{cc} | | +2.7 | +5.5 | V |
| Current Consumption | I _{cc} | 26MHz CMOS output | — | 5 | mA |
| Frequency Aging | f _{age} | 20years @40°C | -4.6 | +4.6 | $\times 10^{-6}$ |
| Voltage Control Range | f _{cont} | Positive *100k ohm min | ± 5 | ± 20 | $\times 10^{-6}$ |
| Output Level | V _{pp} | Clipped Sine, Load: 10k ohm // 10pF | 0.8 | — | V _{p-p} |
| Low Level Output Voltage | V _{OL} | CMOS, Load: 15pF I _{OL} =4mA | — | 10% V _{cc} | V |
| High Level Output Voltage | V _{OH} | CMOS, Load: 15pF I _{OH} =-4mA | 90% V _{cc} | — | V |
| Rise / Fall Time (10%V _{cc} to 90%V _{cc}) | tr/ tf | CMOS, Load: 15pF | — | 5 | ns |
| Symmetry | SYM | 50% V _{cc} | 45 | 55 | % |
| Phase Noise @26MHz | — | - 90 (@10Hz offset) -120 (@100Hz offset) -140 (@1kHz offset) -150 (@10kHz offset) -150 (@100kHz offset) | | | dBc/ Hz |

Dimensions

(Unit: mm)

Recommended Land Pattern

(Unit: mm)

