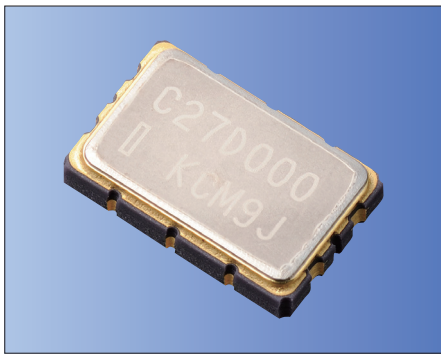




CMOS/ 3.3V/ 5.0×3.2mm



RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage Vcc=3.3V
- Excellent Jitter performance

Table 1

Freq. Code	Tol. $\times 10^{-6}$	Operating Temperature Range (°C)	Note
0	± 50	-10 to +70	Standard specifications
S	± 30	-10 to +70	Please contact us for available frequencies.
G	± 50	-40 to +85	

How to Order

KV5032D 24.576 C 3 □ D 00
① ② ③ ④ ⑤ ⑥ ⑦

- ①Series
- ②Output Frequency
- ③Output Type (CMOS)
- ④Supply Voltage (3.3V)
- ⑤Frequency Tolerance (See Table 1)
- ⑥Symmetry/ INH Function/ Input Resistance (45/ 55%, Disable)
- ⑦Individual Specification (STD Specification is "00".)

Specifications

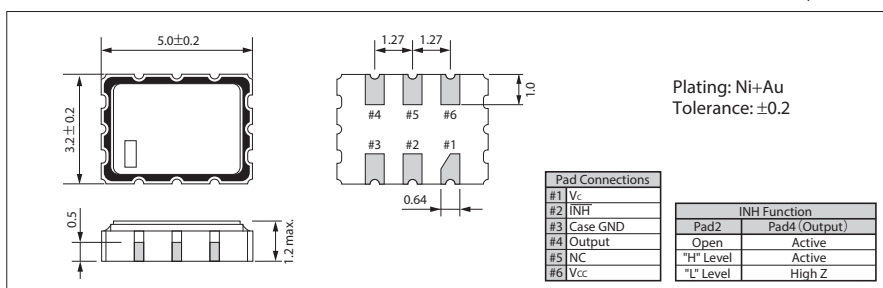
Packaging (Tape & Reel 1000 pcs./ reel)

Item	Symbol	Conditions	Min.	Max.	Unit	
Output Frequency Range ^{Note1}	fo		1.5	170	MHz	
Frequency Tolerance	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Temp.: -10 to +70°C/ -40 to +85°C	-50	+50	$\times 10^{-6}$
			Temp.: -10 to +70°C	-30	+30	
Absolute Pull Range	APR	1.5 ≤ fo ≤ 30MHz 30 < fo ≤ 170MHz	±100 ±50	—	$\times 10^{-6}$	
Control Voltage	V _c		0	+3.3	V	
Storage Temperature Range	T _{stg}		-55	+125	°C	
Operating Temperature Range	T _{use}	Standard Specifications	-10	+70	°C	
		Extend (Option)	-40	+85		
Max. Supply Voltage	—	1.5 ≤ fo ≤ 80MHz 80 < fo ≤ 170MHz	-0.5 -0.5	+7 +5	V	
Supply Voltage	V _{cc}		+2.97	+3.63	V	
Current Consumption	I _{cc}	1.5 ≤ fo ≤ 80MHz	—	15	mA	
		80 < fo ≤ 170MHz	—	35		
Disable Current	I _{dis}	1.5 ≤ fo ≤ 80MHz	—	10	mA	
		80 < fo ≤ 170MHz	—	50		
Symmetry	SYM	@50% V _{cc}	45	55	%	
Rise/ Fall Time (10% V _{cc} to 90% V _{cc})	Tr/ Tf	1.5 ≤ fo ≤ 30MHz	—	8	ns	
		30 < fo ≤ 80MHz	—	5		
		80 < fo ≤ 170MHz	—	4		
Low Level Output Voltage	V _{OL}		—	10% V _{cc}	V	
High Level Output Voltage	V _{OH}		90% V _{cc}	—	V	
Output Load	L _{CMOS}	CMOS Output	—	15	pF	
Input Voltage Range	V _{IN}		0	+3.3	V	
Low Level Input Voltage	V _{IL}		—	30% V _{cc}	V	
High Level Input Voltage	V _{IH}		70% V _{cc}	—	V	
Input Resistance	—	Code⑥ : D	100	—	k ohm	
		Code⑥ : G or N	5	—	Mohm	
Disable Time	t _{dis}		—	100	ns	
Enable Time	t _{ena}	1.5 ≤ fo ≤ 80MHz	—	100	ns	
		80 < fo ≤ 170MHz	—	2	ms	
Start-up Time	t _{str}	@Minimum operating voltage to be 0 sec.	—	10	ms	
Phase Jitter	J _{Phase}	@155.52MHz	BW : 12kHz to 20MHz	—	1.0	ps
Phase Noise	—	@155.52MHz	@10Hz offset	Typ. -55	dBc/ Hz	
			@100Hz offset	Typ. -85		
			@1kHz offset	Typ. -115		
			@10kHz offset	Typ. -130		
			@100kHz offset	Typ. -145		
			@1MHz offset	Typ. -150		
			@10MHz offset	Typ. -155		

Note : All electrical characteristics are defined at the maximum load and operating temperature range.
Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

