



CMOS/ 5.0V/ 7.0×5.0mm



RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage Vcc=5.0V

Table 1

Stability Code	Stability $\times 10^{-6}$	Operating Temperature Range (°C)	Note
0	± 50	-10 to +70	Standard specifications
S	± 30		
U	± 25		
F	± 100	-40 to +85	Please contact us for available frequencies.
G	± 50		
6	± 50	-40 to +105	

How to Order

KC7050A **25.0000** **C** **5** **□** **D** **00**
 ① ② ③ ④ ⑤ ⑥ ⑦

- ①Series
- ②Output Frequency
- ③Output Type (CMOS)
- ④Supply Voltage (5.0V)
- ⑤Frequency Tolerance (See Table 1)
- ⑥Symmetry/ INH Function (45/ 55%, Disable)
- ⑦Individual Specification (STD Specification is "00".)

Packaging (Tape & Reel 1000 pcs./ reel)

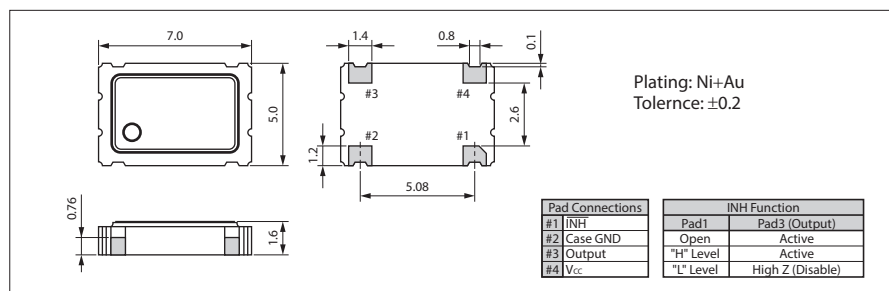
Specifications

Item	Symbol	Conditions	Min.	Max.	Unit	
Output Frequency Range	fo		1.8	50	MHz	
Frequency Tolerance	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Temp.: -40 to +85°C	-100	+100	$\times 10^{-6}$
			Temp.: -10 to +70°C/ -40 to +85°C/ -40 to +105°C	-50	+50	
			Temp.: -10 to +70°C	-30	+30	
			Temp.: -10 to +70°C	-25	+25	
Storage Temperature Range	T _{stg}		-55	+125	°C	
Operating Temperature Range	T _{use}	Standard Specifications	-10	+70	°C	
		Extend (Option)	-40	+85		
Max. Supply Voltage	—		-0.5	+7.0	V	
Supply Voltage	Vcc	Freq. Tol. Code: 0, S, F	+4.5	+5.5	V	
		Freq. Tol. Code: U, G, 6	+4.75	+5.25		
Current Consumption (Maximum Loaded)	Icc	1.8≤fo≤20MHz	—	25	mA	
		20<fo≤40MHz	—	35		
		40<fo≤50MHz	—	50		
Disable Current	I _{dis}		—	20	mA	
Symmetry	SYM	@50% Vcc	45	55	%	
Rise/ Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	Tr/ Tf	1.8≤fo≤26MHz	—	10	ns	
		26<fo≤50MHz	—	8		
Low Level Output Voltage	VoL	I _{oL} =16mA	—	10% Vcc	V	
High Level Output Voltage	VoH	I _{oH} =-16mA	90% Vcc	—	V	
CMOS Load	L _{CMOS}	CMOS Output	—	50	pF	
Input Voltage Range	V _{IN}		0	Vcc	V	
Low Level Input Voltage	V _{IL}		—	0.8	V	
High Level Input Voltage	V _{IH}		2.2	—	V	
Disable Time	t _{dis}		—	100	ns	
Enable Time	t _{ena}		—	100	ns	
Start-up Time	t _{str}	@Minimum operating voltage to be 0 sec.	—	10	ms	
1 Sigma Jitter	J _{Sigma}	Measured with Wavcrest SIA-3000	1.8≤fo<40MHz	—	8	ps
			40≤fo≤50MHz	—	5	
Peak to Peak Jitter	J _{PK-PK}	Measured with Wavcrest SIA-3000	1.8≤fo<40MHz	—	80	ps
			40≤fo≤50MHz	—	40	

Note: All electrical characteristics are defined at the maximum load and operating temperature range.
Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

