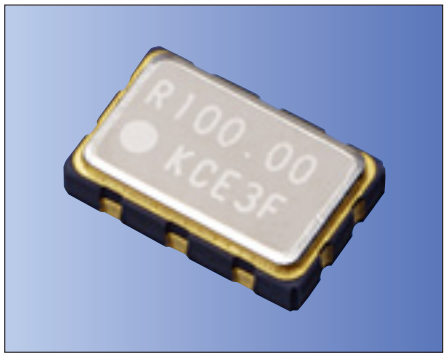
Clock Oscillators Surface Mount Type
KC5032P-P3 Series

KYOCERA

LV-PECL/ 3.3V/ 5.0×3.2mm



RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- LV-PECL output
- Supply voltage Vcc=3.3V
- ±25×10^{-6} available

Table 1

Freq. Tol. Code	Freq. Tol. × 10^{-6}	Operating Temperature Range (°C)	Note
0	± 50	0 to +70	Standard specifications
S	± 30		With only certain frequencies
U	± 25		
F	±100	–40 to +85	
G	± 50		

How to Order

KC5032P 125.000 P 3 0 E 00
① ② ③ ④ ⑤ ⑥ ⑦

① Type (5.0×3.2mm SMD)
② Output Frequency
③ Output Type (LV-PECL)
④ Supply Voltage (3.3V)
⑤ Frequency Tolerance (See Table 1)
⑥ Symmetry/ INH Function (45/ 55%, Stand-by)
⑦ Customer Special Model Suffix (STD Specification is "00")

Packaging (Tape & Reel 1000 pcs./ reel)

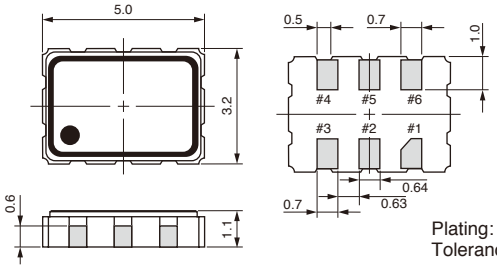
Specifications

Item	Symbol	Conditions		Min.	Max.	Units
Output Frequency Range[Note1]	fo			50	190	MHz
Frequency Tolerance	f_tol	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Op. Temp.: –40 to +85°C	–100	+100	×10^{-6}
			Op. Temp.: 0 to +70°C/ –40 to +85°C	–50	+50	
			Op. Temp.: 0 to +70°C	–30	+30	
			Op. Temp.: 0 to +70°C	–25	+25	
Storage Temperature Range	T_stg			–55	+125	°C
Operating Temperature Range	T_use	Standard Specifications		0	+70	°C
		Extend (Option)		–40	+85	
Max. Supply Voltage	—			–0.5	+7	V
Supply Voltage	Vcc	Freq. Tol.Code: 0, S, F		2.97	3.63	V
		Freq. Tol.Code: U, G		3.14	3.46	
Current Consumption	Icc			—	90	mA
Stand-by Current	I_std			—	30	µA
Symmetry	SYM	50ohm @crossing point		45	55	%
Rise/ Fall Time (20% to 80% Output Level)	tr/ tf	50ohm		—	0.6	ns
Low Level Output Voltage[Note2]	VOL	Op. Temp.: 0 to +85°C/ Typ. 1.600V		Vcc–1.810	Vcc–1.620	V
		Op. Temp.: –40 to 0°C/ Typ. 1.605V		Vcc–1.830	Vcc–1.555	
High Level Output Voltage[Note2]	VOH	Op. Temp.: 0 to +85°C/ Typ. 2.350V		Vcc–1.025	Vcc–0.880	V
		Op. Temp.: –40 to 0°C/ Typ. 2.295V		Vcc–1.085	Vcc–0.900	
Output Load	RL	LV-PECL Output		50		ohm
Input Voltage Range	VIN			0	Vcc	V
Low Level Input Voltage	VIL			—	30% Vcc	V
High Level Input Voltage	VIH			70% Vcc	—	V
Disable Time	t_dis			—	150	ns
Enable Time	t_ena			—	10	ms
Start-up Time	t_str	@Minimum operating voltage to be 0 sec.		—	10	ms
Deterministic Jitter (DJ)	DJ	Measured with Wavecrest DTS-2079 *VISI 6.3.1*		—	2	ps
1 Sigma jitter	Jsigma			—	4	ps
Peak to Peak Jitter	JPK-PK			—	30	ps

Note : All electrical characteristics are defined at the maximum load and operating temperature range.
Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions.
Note2: DC characteristic

Dimensions

(Unit: mm)



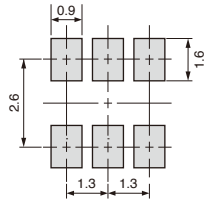
Plating: Ni+Au
Tolerance: ±0.2

Pad Connections	
#1	INH
#2	NC
#3	Case GND
#4	Output
#5	Complementary Output
#6	Vcc

INH Function	
Pad1	Pad4/ Pad5 (Output)
Open	Active
"H" Level	Active
"L" Level	High Z (No-Oscillation)

Recommended Land Pattern

(Unit: mm)



Note: A capacitor of value 0.01µF and 10µF between Vcc and GND is recommended.