



**Pb Free**

**RoHS Compliant**

**Features**

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output CL=50pF max. available
- Supply voltage Vcc=3.3V

**Table 1**

Freq. Tol. Code	Tol. × 10 <sup>-6</sup>	Operating Temperature Range (°C)	Note
0	±50	-10 to +70	Standard specifications
S	±30		With only certain frequencies

**How to Order**

**KC5032C 25.0000 C 3 0 E HL**  
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (5.0×3.2mm SMD)
- ② Output Frequency
- ③ Output Type (CMOS)
- ④ Supply Voltage (3.3V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ Enable Function (45/ 55%, Stand-by)
- ⑦ Heavy Load Type  
HL: CL=50pF max.

Packaging (Tape & Reel 1000 pcs./ reel)

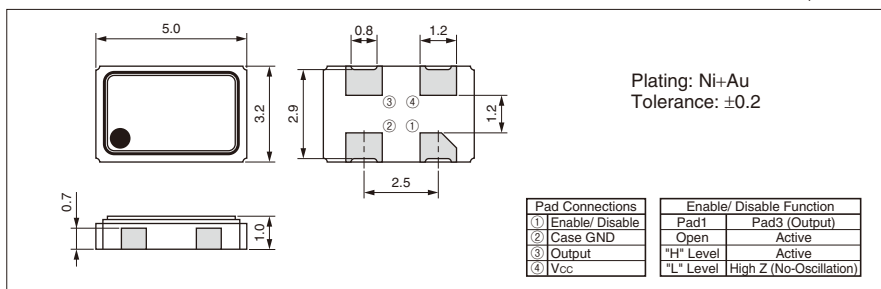
**Specifications**

Item	Symbol	Conditions	Min.	Max.	Units	
Output Frequency Range	f <sub>o</sub>		14	30	MHz	
Frequency Tolerance	f <sub>tol</sub>	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	-50	+50	×10 <sup>-6</sup>	
			-30	+30		
Storage Temperature Range	T <sub>stg</sub>		-55	+125	°C	
Operating Temperature Range	T <sub>use</sub>		-10	+70	°C	
Max. Supply Voltage	—		-0.5	+7	V	
Supply Voltage	V <sub>CC</sub>	Freq. Tol.Code: 0, S	2.97	3.63	V	
Current Consumption (Maximum Loaded)	I <sub>CC</sub>	CL=15pF	—	10	mA	
		CL=50pF	—	15		
Stand-by Current	I <sub>std</sub>		—	10	μA	
Symmetry	SYM	@50% V <sub>CC</sub>	CL=15pF	45	55	%
			CL=50pF	40	60	
Rise/ Fall Time (10% V <sub>CC</sub> to 90% V <sub>CC</sub> Maximum Loaded)	tr/ tf	CL=15pF	—	5	nS	
		CL=50pF	—	8		
Low Level Output Voltage	V <sub>OL</sub>	I <sub>OL</sub> =8mA	—	10% V <sub>CC</sub>	V	
High Level Output Voltage	V <sub>OH</sub>	I <sub>OH</sub> =-8mA	90% V <sub>CC</sub>	—	V	
CMOS Load	L <sub>CMOS</sub>	CMOS Output	—	50	pF	
Input Voltage Range	V <sub>IN</sub>		0	V <sub>CC</sub>	V	
Low Level Input Voltage	V <sub>IL</sub>		—	30% V <sub>CC</sub>	V	
High Level Input Voltage	V <sub>IH</sub>		70% V <sub>CC</sub>	—	V	
Disable Time	t <sub>dis</sub>		—	150	nS	
Enable Time	t <sub>ena</sub>		—	5	mS	
Start-up Time	t <sub>str</sub>	@Minimum operating voltage to be 0 sec.	—	10	mS	
1 Sigma Jitter	J <sub>Sigma</sub>	Measured with Wavcrest DTS-2079 VISI 6.3.1	—	8	pS	
Peak to Peak Jitter	J <sub>PK-PK</sub>		—	80	pS	

Note: All electrical characteristics are defined at the maximum load and operating temperature range.  
 Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

**Dimensions**

(Unit: mm)



**Recommended Land Pattern**

(Unit: mm)

